# Logic Analyzer configuração da EEPROM

The first 8 bytes of the EEPROM must be programmed with the values corresponding to the 8 or 16 channel versions.

### 8 channel Version EEPROM data

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Byte 0 | Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 | Byte 6 | Byte 7 |
| 0xC0 | 0x50 | 0x1D | 0x8C | 0x60 | 0x00 | 0x00 | 0x00 |

### 16 channel Version EEPROM data

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Byte 0 | Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 | Byte 6 | Byte 7 |
| 0xC0 | 0x50 | 0x1D | 0x8D | 0x60 | 0x00 | 0x00 | 0x00 |

### Important

The device with VID 0x1D50 is not automatically detected under Windows (unless it is already installed). Check the Sigrok Pulseview installation guide for more information on how to install the driver.